

UTILITY PATENT APPLICATION TRANSMITTAL

(Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
KIX0103-US

Total Pages in this Submission

TO THE ASSISTANT COMMISSIONER FOR PATENTS

Box Patent Application
Washington, D.C. 20231

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

INFRARED DATA COMMUNICATION MODULE AND METHOD OF MAKING THE SAME

and invented by:

TOMOHARU HORIO

If a **CONTINUATION APPLICATION**, check appropriate box and supply the requisite information:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Which is a:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Which is a:

☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No.: _____

Enclosed are:

Application Elements

1. ☒ Filing fee as calculated and transmitted as described below
2. ☒ Specification having 21 pages and including the following:
 - a. ☒ Descriptive Title of the Invention
 - b. ☐ Cross References to Related Applications (if applicable)
 - c. ☐ Statement Regarding Federally-sponsored Research/Development (if applicable)
 - d. ☐ Reference to Microfiche Appendix (if applicable)
 - e. ☒ Background of the Invention
 - f. ☒ Brief Summary of the Invention
 - g. ☒ Brief Description of the Drawings (if drawings filed)
 - h. ☒ Detailed Description
 - i. ☒ Claim(s) as Classified Below
 - j. ☒ Abstract of the Disclosure

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
KIX0103-US

Total Pages in this Submission

Application Elements (Continued)

3. ☒ Drawing(s) (when necessary as prescribed by 35 USC 113)
- a. ☒ Formal Number of Sheets 16 (SIXTEEN)
- b. ☐ Informal Number of Sheets _____
4. ☒ Oath or Declaration
- a. ☒ Newly executed (original or copy) ☐ Unexecuted
- b. ☐ Copy from a prior application (37 CFR 1.63(d)) (for continuation/divisional application only)
- c. ☒ With Power of Attorney ☐ Without Power of Attorney
- d. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting inventor(s) named in the prior application,
see 37 C.F.R. 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference (usable if Box 4b is checked)
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. ☐ Computer Program in Microfiche (Appendix)
7. ☐ Nucleotide and/or Amino Acid Sequence Submission (if applicable, all must be included)
- a. ☐ Paper Copy
- b. ☐ Computer Readable Copy (identical to computer copy)
- c. ☐ Statement Verifying Identical Paper and Computer Readable Copy

Accompanying Application Parts

8. ☒ Assignment Papers (cover sheet & document(s))
9. ☐ 37 CFR 3.73(B) Statement (when there is an assignee)
10. ☐ English Translation Document (if applicable)
11. ☒ Information Disclosure Statement/PTO-1449 ☒ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Acknowledgment postcard
14. ☐ Certificate of Mailing
- ☐ First Class ☐ Express Mail (Specify Label No.): _____

UTILITY PATENT APPLICATION TRANSMITTAL (Large Entity)

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Docket No.
KIX0103-US

Total Pages in this Submission

Accompanying Application Parts (Continued)

15. ☐ Certified Copy of Priority Document(s) (if foreign priority is claimed)

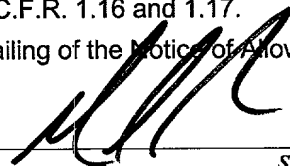
16. ☐ Additional Enclosures (please identify below):

Fee Calculation and Transmittal

CLAIMS AS FILED

For	#Filed	#Allowed	#Extra	Rate	Fee
Total Claims	20	- 20 =	0	x \$18.00	\$0.00
Indep. Claims	3	- 3 =	0	x \$78.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00
BASIC FEE					\$690.00
OTHER FEE (specify purpose) Assignment Recordation Fee					\$40.00
TOTAL FILING FEE					\$730.00

- ☒ A check in the amount of \$730.00 to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 50-1390 as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of as filing fee.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.
- ☐ Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.311(b).



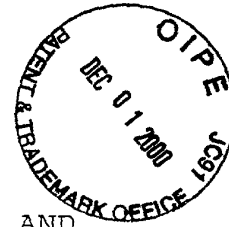
Signature

Michael D. Bednarek
Regn. No. 32,329
Shawpittman
2300 N Street, N.W.
Washington, D.C. 20037-1128

Dated: AUGUST 31, 2000

CC:

SPECIFICATION



INFRARED DATA COMMUNICATION MODULE AND METHOD OF MAKING THE SAME

5

TECHNICAL FIELD

The present invention relates to an infrared data communication module for performing data communication under so-called IrDa (Infrared Data Association).

10

BACKGROUND ART

An infrared data communication module for operation under IrDA is commonly used in the field of notebook personal computers, and recently, it is also used in a mobile phone or an electronic scheduler for example. Such an infrared data communication module includes an infrared LED, a photodiode and a modem circuit as enclosed in a single package for performing two-way wireless communication.

While the communication performance of the infrared data communication module is enhancing, there is an increasing demand for size reduction of the infrared data communication module. Moreover, dimensional accuracy is strictly required in manufacturing the infrared data communication module.

Figs. 20 and 21 illustrate an example of prior-art method of making infrared data communication modules. According to the prior art method, use is made of a strip-like or rectangular substrate 1e on which plural sets of infrared

light emitting elements 2e and infrared light receiving elements 3e are arranged in rows and then sealed in resin packages 4e.

In such a method, the number of the resin packages 4e is the same as the number of the sets of light emitting elements 2e and light receiving elements 3e, so that each resin package separately encloses a respective set of light emitting element 2e and light receiving element 3e. After the resin-packaging, the substrate 1e is cut at positions indicated by phantom lines L1, L2, thereby providing a plurality of infrared data communication modules.

However, the above-described conventional manufacturing method has the following problems.

According to the conventional method, a resin package 4e is provided separately for each set of light emitting element 2e and light receiving element 3e. This leads to an increase in the total number of the resin packages 4e while also increasing the number of clearances 90 provided between the resin packages 4e, thereby wasting the space of the substrate 1e. As a result, the number of infrared data communication modules obtainable from the substrate 1e of a given size decreases, which leads to an increase in the manufacturing cost of the infrared data communication modules.

To solve the problem described above, the inventor of the present invention has previously conceived the idea of causing a single resin package to enclose, on the substrate

1e, a group of components including plural sets of light emitting elements 2e and light receiving elements 3e. With such a method, it is possible to minimize the number of the resin packages for eliminating the wasted space (clearances
5 90) between the resin packages.

With the above measure, however, the resin package comes into intimate contact with the substrate 1e over a large surface area. Therefore, in the case where the substrate 1e has a small thickness or is made of a relatively soft material,
10 the substrate 1e warps, consequently distorting the infrared data communication modules obtained from the substrate.

Further, as shown in Fig. 22, use is made of mold members P1, P2 for molding a resin package 4e to seal a light emitting element 2e and a light receiving element 3e on a substrate
15 1e. Specifically, the head mold member P1 having a configuration for defining the configuration of the resin package 4e presses against the obverse surface of the substrate 1e, whereas the tail mold member P2 which has a flat pressing surface presses against the reverse surface of the
20 substrate 1e. Then, with the tail mold member P2 held in intimate contact with the reverse surface of the substrate 1e, resin is injected between the head mold member P1 and the obverse surface of the substrate 1e for solidification into the resin package 4e.

25 The substrate 1e is formed with through-holes 7 extending thicknesswise. The substrate 1e is further formed, on the reverse surface thereof, with terminals 71 connected

to the through-hole 7, respectively. Since each of the terminals 71 made of a thin conductor film has a certain thickness, the reverse surface of the substrate 1e may become irregular. Therefore, the tail mold member P2 may not come
5 into intimate contact with the substrate 1e, and the surface pressure against the reverse surface of the substrate 1e may be insufficient around the through-holes 7. As a result, a clearance may be formed between the reverse surface of the substrate 1e and the tail mold member P2. When resin is filled
10 between the head mold member P1 and the substrate 1e in such a condition, the resin may flow via the through-holes onto the reverse surface of the substrate 1e. As a result, resin may be deposited on the reverse surface of the substrate 1e.

Moreover, before the sealing step and the cutting step
15 described above, a predetermined conductor pattern (not shown) and the terminals 71 are formed on the obverse and the reverse surfaces of the substrate 1e, respectively, by photolithography for example. Specifically, a resist material is applied on the substrate 1e which is initially
20 formed with a copper film on a surface thereof. Then, a mask having a predetermined pattern disposed on the substrate which is then subjected to light exposure, development and etching for removing unnecessary portions of the copper film.

Then, an insulating layer (not shown) called "green
25 resist" is formed on the substrate 1e to cover portions of the substrate other than the conductor pattern and the terminals 71 which are to be exposed. For forming such an

insulating layer, a similar mask is utilized for light exposure. At this time, if the mask positionally deviates, the exposed area of the conductor pattern and the terminals 71 may become small.

5 An infrared data communication module A may be mounted on a module mounting board 9 so that the reverse surface of the substrate 1e extends perpendicularly to an obverse surface of the module mounting board 9, as shown in Fig. 23. In this case, the terminals 71 on the reverse surface of the
10 substrate and a wiring pattern P formed on the module mounting board 9 are bonded together solder.

 However, if the exposed portions for the terminals 71 are small as described above, solder fillets are not sufficiently formed or likely to be easily detached.
15 Therefore, the terminals 71 of the infrared data communication module and the wiring pattern P of the module mounting board 9 are not reliably connected.

DISCLOSURE OF THE INVENTION

20 An object of the present invention is to provide a method of making an infrared data communication modules which is capable of eliminating or lessening the problems described above while also providing such an infrared data communication module.

25 In accordance with a first aspect of the present invention, there is provided a method of making infrared data communication modules comprising the steps of: forming

predetermined wiring patterns on an obverse and a reverse surfaces of a substrate; mounting, on one of the surfaces of the substrate, a group of components including plural sets of light emitting elements and light receiving elements;
5 resin-packaging the group of components mounted on the substrate; and dividing the resin-packaged components into a plurality of infrared data communication modules each of which includes a respective set of light emitting element and light receiving element; wherein the resin-packaging step
10 comprises forming a plurality of mutually separated resin packages each of which collectively seals at least two sets of light emitting elements and light receiving elements.

According to this method, the number of the resin packages needed is smaller than the number of the sets of light emitting elements and light receiving elements. Therefore,
15 the number of clearances provided between the resin packages can be reduced so that the total area of the clearances can be reduced. As a result, the number of infrared data communication modules obtainable from a single substrate is
20 larger than is obtainable in the prior art, which leads to a cost reduction in manufacturing the infrared data communication modules.

Moreover, unlike the prior art in which a group of components mounted on a surface of the substrate is sealed
25 in a single resin package, an appropriate number of clearances are provided between the plural resin packages, so that the surface of the substrate includes portions which are not held

in intimate contact with the resin packages. Therefore, warping of the substrate due to the formation of the resin packages on one surface thereof can be reliably prevented or reduced. As a result, the produced infrared data communication modules are free from strains to provide a high quality.

Preferably, the mounting step includes arranging the plural sets of light emitting elements and light receiving elements in a matrix on said one surface of the substrate, and the resin-packaging step includes forming the packages so as to be arranged in a matrix.

According to the above, the substrate is less likely to warp both longitudinally and widthwise of the substrate. Therefore, it is possible to further enhance the quality of the infrared data communication modules finally obtained from the substrate.

Preferably, the substrate is elongated in one direction to be rectangular or strip-like. The substrate is formed with a plurality of slits extending widthwise of the substrate and spaced from each other longitudinally of the substrate. The group of components is mounted on said one surface of the substrate in each of regions defined between the slits.

According to the above, the substrate is likely to be warped longitudinally at the slits. Therefore, in resin-packaging the components mounted in a region between each two slits, even if a bending stress is applied to the substrate, such a stress is absorbed and alleviated by the deformation

of the substrate at the slits. As a result, it is possible to prevent such a stress from largely affecting the adjacent region. Therefore, the substrate can be more reliably prevented from flexing at the regions on which the components
5 are mounted.

Preferably, the pattern forming step includes forming, on the reverse surface of the substrate, terminals for connection to through-holes penetrating the substrate thicknesswise together with dummy patterns which are
10 substantially equal in thickness to the terminals.

According to such a method, the dummy patterns formed on the reverse surface of the substrate to have substantially the same thickness as the terminals connected to the through-holes provides a sufficient surface pressure when the
15 substrate is compressively held by a mold. Therefore, the dummy patterns held in firm contact with the mold on the reverse surface of the substrate prevents resin, which enters into the through-holes from the obverse surface of the substrate, from flowing out onto the reverse surface of the
20 substrate.

Preferably, the pattern forming step includes forming, on the reverse surface of the substrate, terminals for connection to through-holes penetrating the substrate thicknesswise. The terminals are elongated to be
25 substantially rectangular for bonding to an external mounting board.

When an insulating layer is formed on the reverse surface of the substrate, by photolithography for example, in such a manner as to expose the terminals, the mask for light exposure may be disposed at a deviated position. However, 5 since the terminals are elongated into a generally rectangular configuration according to this method, such a deviation may be allowed longitudinally of the terminals. Further, the elongated terminals provide a sufficient area utilized for effectively bonding to an external module- 10 mounting board.

In accordance with a second aspect of the present invention, there is provided an infrared data communication module made by the steps of: forming predetermined wiring patterns on an obverse and a reverse surfaces of a substrate; 15 mounting, on one of the surfaces of the substrate, a group of components including plural sets of light emitting elements and light receiving elements; resin-packaging the group of components mounted on the substrate; and dividing the resin-packaged components into a plurality of infrared 20 data communication modules each of which includes a respective set of light emitting element and light receiving element; wherein the resin-packaging step comprises forming a plurality of mutually separated resin packages each of which collectively seals at least two sets of light emitting 25 elements and light receiving elements.

The structure described above provides the same advantages as the first aspect of the present invention.

Preferably, the substrate may be formed with through-holes penetrating the substrate thicknesswise, and the reverse surface of the substrate may be formed with terminals for connection to the through-holes and dummy patterns which are substantially equal in thickness to the terminals.

Preferably, the obverse surface of the substrate may include sub-areas each for mounting a respective set of light emitting element and light receiving element, and the wiring patterns may be formed respectively in the sub-areas. The dummy patterns may correspond in position and in general configuration to the wiring patterns.

According to this structure, the wiring patterns having a certain thickness are formed at the sub-areas on the obverse surface of the substrate, whereas the dummy patterns having a corresponding thickness are formed on the reverse surface of the substrate in corresponding relation to the wiring patterns. Thus, the surface pressure applied by the mold are sufficiently increased so that the substrate and the mold are held in intimate contact with each other. As a result, the sub-areas can be reliably sealed with resin to reliably produce infrared data communication modules at respective sub-areas.

Preferably, the substrate may be formed with through-holes penetrating the substrate thicknesswise. The reverse surface of the substrate may be formed with terminals for connection to the through-holes and for bonding to an

external mounting board. The terminals may be elongated to be substantially rectangular.

According to this structure, a solder fillet can be formed in an amount sufficient for strongly soldering each
5 terminal on the reverse surface of the substrate to the wiring pattern on the module mounting board. Therefore, it is possible to increase the mounting strength of the infrared data communication module on the module mounting board.

Preferably, the terminals may be so formed as to projects
10 from an obverse surface of the mounting board when the substrate is mounted on the mounting board with the reverse surface of the substrate oriented perpendicularly to the obverse surface of the substrate.

Since the wiring pattern on the module mounting board
15 has a predetermined width, there is a limitation on enlarging the terminals on the reverse surface of the substrate longitudinally thereof. However, it is possible to extend the terminals perpendicularly to the obverse surface of the module mounting board as much as possible the width of the
20 substrate allows. Therefore, with the above-described structure, it is possible to form a sufficient amount of solder fillet.

Other features and advantages of the present invention will become clearer from the description of embodiments given
25 below with reference to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a perspective view of a substrate used for making infrared data communication modules in accordance with a first embodiment of the present invention.

5 Fig. 2 is a perspective view of the substrate shown in Fig. 1 as viewed from the reverse side.

Fig. 3 is a sectional view taken along lines III-III in Fig. 1.

10 Fig. 4 is a sectional view taken along lines IV-IV in Fig. 1.

Fig. 5 is a perspective view of the substrate on which components are mounted.

Fig. 6 is a sectional view taken along lines VI-VI in Fig. 5.

15 Fig. 7 is a sectional view taken along lines VII-VII in Fig. 5.

Fig. 8 is a perspective view showing a sealing step in making infrared data communication modules.

20 Fig. 9 is a perspective view of the substrate provided with resin packages.

Fig. 10 is a sectional view taken along lines X-X in Fig. 9.

Fig. 11 is a sectional view taken along lines XI-XI in Fig. 9.

25 Fig. 12 is a sectional view showing a cutting step in making infrared data communication modules.

Fig. 13 is a sectional view showing the cutting step in making infrared data communication modules.

Fig. 14 is sectional view showing a mode for mounting an infrared data communication module.

5 Fig. 15 is a sectional view taken along lines XV-XV in Fig. 14.

Fig. 16 is a side view of the infrared data communication module.

Fig. 17 is sectional view showing another mode for mounting the infrared data communication module.
10

Fig. 18 is a perspective view of an infrared data communication module in accordance with a second embodiment of the present invention.

Fig. 19 is a perspective view showing the infrared data communication module of Fig. 18 in its mounted state.
15

Fig. 20 is a sectional view showing a substrate used in a conventional method of making infrared data communication modules.

Fig. 21 is a sectional view taken along lines XXI-XXI in Fig. 20.
20

Fig. 22 is a sectional view showing a substrate and a mold used in the conventional method of making infrared data communication modules.

Fig. 23 is a perspective view showing a conventional infrared data communication module in its mounted state.
25

BEST MODE FOR CARRYING OUT THE INVENTION

Preferred embodiments of the present invention will be described below with reference to the accompanying drawings. Throughout the drawings, identical or similar elements are designated by the same reference signs.

First, reference is made to Figs. 1 through 13 which illustrate a method of making an infrared data communication module according to a first embodiment of the present invention. For making an infrared data communication module, use is made of a substrate 1, as shown in Figs. 1 and 2. Fig. 1 shows the obverse side of the substrate 1, whereas Fig. 2 shows the reverse side of the substrate 1.

The substrate 1 may be made of glass-fiber-reinforced epoxy resin for example and has a rectangular or strip-like configuration elongated in one direction. The substrate 1 is formed with a plurality of slits 18 spaced from each other longitudinally of the substrate 1. Each of the slits 18 is narrow and extends widthwise of the substrate 1. The substrate 10 has an obverse surface 10a which provides component mounting regions S each between respective two adjacent slits 18 for mounting components, as described later. Each of the component mounting regions S includes a plurality of sub-areas 19 and a conductor layer 10 surrounding each of the sub-areas 19.

The plurality of sub-areas 19, each of which is rectangular, are arranged in a matrix including a plurality of rows and columns suitably spaced longitudinally and

widthwise of the substrate 1. Each of the sub-areas 19 is formed with a wiring pattern 70 (partially omitted in the illustration) which is necessary for providing one infrared data communication module. The wiring pattern includes electrodes and pads for mounting a light emitting element, a light receiving element and an LSI chip. In Fig 1, only pads are illustrated.

As shown in Fig. 2, the substrate 1 has a reverse surface 10b which provides dummy areas 21 in corresponding relation to the sub-areas 19, respectively. The dummy areas 21 are identical in size to the sub-areas 19. A conductor layer 20 is so formed as to surround each of the dummy areas 21. Each dummy area 21 includes a dummy pattern 22 which is so sized as to be substantially identical in total area to the wiring pattern 70 of a corresponding sub-area 19. The dummy pattern 22 has a thickness substantially identical to that of the conductor layer 20.

As shown in Figs. 3 and 4, the sub-areas 19 are provided by forming a thin film conductor layer 10 entirely over the obverse surface 10a of the substrate 1 and then partially removing, by etching for example, rectangular portions of the conductor layer 10 corresponding to the sub-areas 19. Upon etching, part of the conductor layer 10 remains within each sub-area 19 to provide a wiring pattern 70.

Like the sub-areas, the dummy areas 21 are provided by forming a thin film conductor layer 20 entirely over the reverse surface 10b of the substrate 1 and then partially

removing, by etching for example, rectangular portions of the conductor layer 20 corresponding to the dummy areas 21. Upon etching, part of the conductor layer 20 remains within each dummy area 21 to provide a dummy pattern 22. The conductor
5 layer 20 including the dummy patterns 22 makes the reverse surface 10b of the substrate 1 less irregular.

Each of the wiring patterns 70 is electrically connected to a plurality of through-holes 7 aligned widthwise of the substrate 1. Each of the through-holes 7 extends through the
10 thickness of the substrate 1. The through-hole 7 includes a conductor layer 7a formed on its inner circumferential surface for conduction with each of plural terminals 71 formed on the reverse surface 10b of the substrate 1. The terminals 71 are connected to the conductor layer 20 on the reverse
15 surface 10b of the substrate 1.

Each of the sub-areas 19 is provided with a respective set of components including a light emitting element 2, a light receiving element 3 and an LSI chip 6 mounted on the respective pads of the wiring pattern 70, as shown in Figs. 5 through
20 7. The light emitting element 2 may comprise an infrared light emitting diode for example. The light receiving element 3 may comprise a PIN photodiode capable of detecting infrared. The LSI chip 6 controls the infrared emission and reception of the light emitting element 2 and the light
25 receiving element 3, respectively. Specifically, the LSI chip 6 includes a modem circuit and a waveform shaping circuit. The light emitting element 2, the light receiving element 3

and the LSI chip 6 in each set are aligned in a row extending widthwise of the substrate 1.

The light emitting element 2, the light receiving element 3 and the LSI chip 6 are connected to the electrodes of the wiring pattern 70 by wire-bonding. The light emitting element 2, the light receiving element 3 and the LSI chip 6 are electrically connected to the through-holes 7 via the wiring pattern 70.

After mounted on the substrate 1, the above-mentioned components are resin-packaged. The resin-packaging step is performed in a manner such that a plurality of resin packages 4 separate from each other are formed on each component mounting region S on the obverse surface 10a of the substrate 1, as shown in Figs. 8 and 9. Each of the resin packages 4 may be formed of epoxy resin containing pigment for example, and has a property of blocking visible light while reliably passing infrared.

The resin packages 4 may be formed by transfer molding for example using a head and a tail mold members P1, P2, as shown in Fig. 8. The head mold member P1 includes cavities 4a which correspond in configuration to the resin packages 4. The tail mold member P2 has a flat pressing surface P2a for facing the substrate 1 from its reverse side.

The cavities 4a of the head mold member P1 in each row extending widthwise of the substrate 1 communicate with each other via a respective gate (not shown). The head mold member P1 is precisely positioned relative to the upper surface 10a

of the substrate 1 so that each cavity 4a enclose two sub-areas 19. The tail mold member P2 presses against the entire reverse surface 10b of the substrate 1.

5 The substrate 1 is compressively held between the upper and tail mold members P1, P2. After resin is injected via runners 4b into the cavities 4a and caused to harden, the mold members P1, P2 are opened, thereby providing a molded product, as shown in Fig. 9.

10 As shown in Figs. 10 and 11, each of the resin packages 4 thus provided has a plurality of side surfaces 40 obliquely rising from the obverse surface 10a of the substrate 1. The resin package 4 further includes an upper surface 42 connected to upper edges 42 of the side surfaces 40, . The side surfaces 40 are inclined because the cavities 4a the head mold member 15 P1 need to have drafts for facilitating mold opening.

The upper surface 41 is provided with a pair of hemispherical lens portions 43a, 43b formed by bulging the upper surface upwardly. The lenses 43a, 43b provide directivity for light transmission of the light emitting 20 element 2 and light reception of the light receiving element 3.

As viewed longitudinally of the substrate 1, each of the thus molded resin packages collectively seals two sets of light emitting elements 2, light receiving elements 3 and LSI 25 chips 6 respectively mounted on two adjacent sub-areas 19. Therefore, when eight sets of light emitting elements 2, light receiving elements 3 and LSI chips 6 are arrayed

longitudinally of the substrate 1 in a respective component mounting region S, four resin packages 4 are provided in a row extending in that direction. Three clearances 92 of a suitable width are provided between the four resin packages

5 4.

As viewed widthwise of the substrate 1, each of the resin packages 4 is formed to seal a set of light emitting element 2, light receiving element 3 and LSI chip 6. Clearances 93 of a suitable width are formed between the resin packages 4.

10 According to the molding step described above, the total number of the clearances 92 provided longitudinally of the substrate 1 can be reduced in comparison with the case where a resin package is provided for each sub-area 19. Therefore, wasting of space of the substrate 1 can be reduced as much
15 as the total number of the clearances 92 is reduced, thereby increasing the number of infrared data communication modules obtainable from a single substrate 1. Moreover, since an appropriate number of clearances 92, 93 are still provided between the resin packages 4 longitudinally and widthwise of
20 the substrate 1, the substrate is prevented from warping to a large extent longitudinally and/or widthwise due to intimate contact between the resin packages and the obverse surface 10a of the substrate 1.

The above-described resin-packaging step is performed
25 successively for the component mounting regions S arranged longitudinally of the substrate 1. Since two adjacent component mounting regions S are divided by a slit 18, the

substrate 1 is readily bendable at the slit 18. Therefore, even if a bending force is applied to any component mounting region S, such a force is absorbed by the deformation of the substrate 1 at the relevant slit 18.

5 In this way, a bending force applied to any component mounting region S on the substrate 1 does not adversely affects an adjacent component mounting region. As a result, it is possible to more reliably prevent the substrate 1 from flexing in each component mounting region S.

10 Instead of collectively sealing two sets of light emitting elements 2 and light receiving elements 3, each resin package 4 may collectively seal three or more sets of such components. As the number of the component sets sealed by a single resin package increases, the number of the clearances
15 between the resin packages 4 decreases. Thus, it is possible to increase the total number of infrared data communication modules obtainable from a single substrate.

 In molding the resin packages 4, resin on each sub-area 19 may flow via the through-holes 7 onto the reverse surface
20 10b of the substrate 1. Even if the openings of the through-holes 7 are closed by a suitable resist film (not shown) to prevent the resin from flowing into the through-holes, the resist film in contact with the hot resin may melt. Also in such a case, the resin may flow via the
25 through-holes 7 onto the reverse surface 10b of the substrate 1.

However, according to the present invention, the reverse surface 10b of the substrate 1 is formed with dummy patterns 22 in addition to the terminals 71 around the through-holes 7, and the dummy patterns 22 are identical in thickness with the terminals 71. As a result, the reverse surface of the substrate is generally planer with little irregularities.

Therefore, when the upper and tail mold members P1, P2 press against the substrate 1, the surface pressure against the sub-areas 19 and the dummy areas 21 is increased. Accordingly, the pressing surface P2a (See Fig. 8) of the tail mold member P2 comes into intimate contact with the terminals 71 and the dummy patterns 22 around the through-holes 7 on the reverse surface 10b of the substrate 1 without any clearance. In other words, the through-holes 7 are reliably closed by the flat pressing surface P2a of the tail mold member P2 on the reverse surface 10b of the substrate 1. Therefore, even if resin flows into the through-holes 7 from the obverse surface 10a of the substrate 1, the resin does not flow out of the through-holes 7 onto the reverse surface 10b. As a result, it is possible to prevent the resin from adhering to the reverse surface 10b of the substrate 1.

After the resin-packaging step is completed, the substrate 1 together with the resin packages 4 formed thereon are cut to divide the substrate 1 and the mounted components into a plurality of infrared data communication modules, as shown in Figs. 12 and 13. At this time, the substrate 1 is cut both longitudinally and widthwise thereof.

The substrate 1 may be cut with a rotary discal blade 5. For longitudinal division, the substrate 1 may be cut at portions indicated by phantom lines La~Ld in Fig. 12. The phantom lines La~Ld traverse the upper surface 41 of each resin package 4. Each of the phantom lines La, Ld at two widthwise opposite sides of the package is located inwardly from the upper edge 42 of a side surface 40 of a respective resin package 4 by a predetermined amount Sa. Therefore, when the substrate 1 and the resin packages 4 are cut along the phantom lines La, Ld, the inclined side surfaces 40 are cut away from the resin package 4. The phantom lines La extend through the through-holes 7 so that the through-holes 7 are divided when the substrate 1 is cut.

Each of the phantom lines Lb, Lc is located generally at a central portion widthwise of a respective resin package 4. By performing cutting along the phantom lines Lb, Lc, each of the resin packages 4 is divided to provide two infrared data communication modules each of which includes a set of light emitting element 2, light receiving element 3 and LSI chip 6. The phantom lines Lc also extend through the through-holes 7 so that the through-holes 7 are divided when the substrate 1 is cut.

If the cutting blade 5 has a thickness t which is equal to the distance between the phantom lines Lb and Lc, the cutting along these phantom lines Lb and Lc can be performed by a single cutting operation. Therefore, it is possible to reduce the number of cutting operations.

For widthwise division of the substrate 1, the substrate 1 and the resin packages 4 may be cut at positions indicated by phantom lines Le for example, as shown in Fig. 13. Similarly to the cutting operation performed at the phantom lines La, Ld shown in Fig. 12, the resin packages 4 are cut for removing the inclined side surfaces 40 of the resin packages 4.

However, the cutting operation may be performed so as to partially or entirely retain the inclined side surfaces 40 of the resin packages 4. For example, instead of cutting the substrate 1 and the resin packages 4 along the phantom lines Le, only the substrate 1 may be cut at positions indicated by phantom lines Lg shown in Fig 13.

By performing the above-described steps in succession, a plurality of infrared data communication modules are provided, each of which is configured as shown in Figs. 14 through 16. The infrared data communication module thus provided includes a light emitting element 2, a light receiving element 3 and an LSI chip 6 as mounted on a rectangularly cut substrate 1a and as sealingly enclosed in a rectangularly cut resin package 4a.

The resin package 4a no longer has the previously present side surfaces 40 obliquely rising from the obverse surface of the substrate 1a. Instead, the resin package 4a has side surfaces 40a each of which is a smooth flat cut surface in flush with a corresponding cut surface 11 of the substrate 1a. Specifically, the infrared data communication module A

includes two flat surfaces 8A and two flat surfaces 8B, each flat surface comprising a cut surface 11 and a side surface 40a connected to and flush with each other. Each flat surface 8A extends longitudinally of the infrared data communication module A, whereas each flat surface 8B extends perpendicularly to the flat surfaces 8A. One of the flat surfaces 8A is formed with a plurality of grooves 7A formed by dividing the plurality of through-holes 7. The conductor layers 7a connected to the respective terminals 71 are exposed at the grooves 7A.

The infrared data communication modules A having the above structure is made from the master substrate 1 which is not largely flexed before being divided into the cut substrates 1a. Therefore, die-bonding and wire-bonding portions for the light emitting element 2, the light receiving element 3 and the LSI chip 6 mounted on the substrate 1a are not flexed either, thereby providing a good quality for the infrared data communication module A.

The infrared data communication module 1 may be used in the following two different modes for example. In a first mode, the infrared data communication module A is mounted on a module mounting board 9 with the reverse surface 10b of the substrate 1a oriented downward, as shown in Fig. 14.

For such mounting, the terminals 71 on the reverse surface 10b of the substrate 1a are bonded onto corresponding terminals 94 on the module mounting board 9 by reflow soldering for example. The solder may be applied so as to contact not

only the terminals 71 but also the conductor layers 7a at the grooves 7A. Therefore, it is possible to increase the mounting strength of the infrared data communication module A onto the module mounting board 9. According to the first mode, infrared is transmitted and received perpendicularly to the obverse surface of the module mounting board 9.

Fig. 17 illustrates a second mode for using the infrared data communication module A. In this second mode, the infrared data communication module A is mounted with one of the surfaces 8A oriented downward so that the conductor layers 7a at the grooves 7A face the terminals 94 of the module mounting board 9. Since the surface 8A has a large width which is the sum of the thickness of the substrate 1a and the thickness of the resin package 4a excluding the lens portions 43a, 43b, the infrared data communication module A can be stably carried on the module mounting board 9.

In this way, the infrared data communication module A may be conveniently mounted onto the module mounting board 9 by reflow soldering. In soldering the conductor layers 7a onto the terminals 94, the solder may be brought into contact with the terminals 71. Thus, it is possible to increase the mounting strength of the infrared data communication module A.

Now, reference is made to Figs. 18 and 19 which illustrate an infrared data communication module according to a second embodiment of the present invention. The infrared data communication module A is formed with terminals 71 each of

which is elongated into a generally rectangular configuration. The terminals 71 are formed instead of the dummy pattern 22 in the dummy area 21 on the reverse surface 10b of the substrate 1 in the first embodiment.

5 Generally, in forming an infrared data communication module A, an insulating layer is formed, by photolithography for example, so as to cover the reverse surface 10b of a substrate 1 but to expose the terminals 71. In forming such an insulating layer, the light exposure step is performed with
10 the use of a mask which is formed with apertures positionally corresponding to the terminals 71. Then, unnecessary portions of the insulating layer are removed by development.

 According to the second embodiment, since the terminals 71 are elongated rectangularly, the mask may positionally
15 deviate longitudinally of the terminals 71 to a certain extent. Further, since the terminals 71 provide a sufficient area, they can be advantageously utilized for bonding to the module mounting board 9.

 Specifically, as shown in Fig. 19, the reverse surface
20 10a is oriented perpendicularly to the obverse surface of the module mounting board 9 in mounting the infrared data communication module A onto the module mounting board 9 by soldering. At this time, a solder fillet F is formed between each terminal 71 of the substrate 1 and a corresponding wiring
25 pattern P of the module mounting board 9. Since the terminal 71 is elongated, the amount of the solder fillet F may be rendered appropriately large. As a result, the terminal 71

and the wiring pattern P can be firmly bonded, increasing the mounting strength of the infrared data communication module A onto the module mounting board 9.

More specifically, each terminal 71 extend
5 perpendicularly to the obverse surface of the module mounting board 9. Since the wiring pattern P on the module mounting board 9 has a limited width, there is a limitation on increasing the width of the terminals 71 on the reverse surface 10b of the substrate 1a longitudinally thereof. However, it
10 is possible to elongate the terminal 71 perpendicularly to the obverse surface of the module mounting board 9 as long as possible within the width of the substrate 1a. Therefore, a sufficient amount of solder fillet can be formed in that direction at the terminal 71 so that the terminal 71 and the
15 wiring pattern P are joined strongly.

CLAIMS

1. A method of making infrared data communication modules comprising the steps of:

5 forming predetermined wiring patterns on an obverse and a reverse surfaces of a substrate;

 mounting, on one of the surfaces of the substrate, a group of components including plural sets of light emitting elements and light receiving elements;

10 resin-packaging the group of components mounted on the substrate; and

 dividing the resin-packaged components into a plurality of infrared data communication modules each of which includes a respective set of light emitting element and light receiving
15 element;

 wherein the resin-packaging step comprises forming a plurality of mutually separated resin packages each of which collectively seals at least two sets of light emitting elements and light receiving elements.

20

2. The method of making infrared data communication modules according to claim 1,

 wherein the mounting step includes arranging the plural sets of light emitting elements and light receiving elements
25 in a matrix on said one surface of the substrate; and

 wherein the resin-packaging step includes forming the packages so as to be arranged in a matrix.

3. The method of making infrared data communication modules according to claim 1, wherein the substrate is elongated in one direction to be rectangular or strip-like, the substrate being formed with a plurality of slits extending widthwise
5 of the substrate and spaced from each other longitudinally of the substrate, the group of components being mounted on said one surface of the substrate in each of regions defined between the slits.

10 4. The method of making infrared data communication modules according to claim 1, wherein the pattern forming step includes forming, on the reverse surface of the substrate, terminals for connection to through-holes penetrating the substrate thicknesswise together with dummy patterns which
15 are substantially equal in thickness to the terminals.

5. The method of making infrared data communication modules according to claim 1, wherein the pattern forming step includes forming, on the reverse surface of the substrate,
20 terminals for connection to through-holes penetrating the substrate thicknesswise, the terminals being elongated to be substantially rectangular for bonding to an external mounting board.

25 6. An infrared data communication module made by the steps of: forming predetermined wiring patterns on an obverse and a reverse surfaces of a substrate; mounting, on one of the

surfaces of the substrate, a group of components including plural sets of light emitting elements and light receiving elements; resin-packaging the group of components mounted on the substrate; and dividing the resin-packaged components
5 into a plurality of infrared data communication modules each of which includes a respective set of light emitting element and light receiving element;

wherein the resin-packaging step comprises forming a plurality of mutually separated resin packages each of which
10 collectively seals at least two sets of light emitting elements and light receiving elements.

7. The infrared data communication module according to claim 6,

15 wherein the substrate is formed with through-holes penetrating the substrate thicknesswise; and

wherein the reverse surface of the substrate is formed with terminals for connection to the through-holes and dummy patterns which are substantially equal in thickness to the
20 terminals.

8. The infrared data communication module according to claim 7,

wherein the obverse surface of the substrate includes
25 sub-areas each for mounting a respective set of light emitting element and light receiving element;

wherein the wiring patterns are formed respectively in the sub-areas; and

wherein the dummy patterns correspond in position and in general configuration to the wiring patterns.

5

9. The infrared data communication module according to claim 6,

wherein the substrate is formed with through-holes penetrating the substrate thicknesswise;

10

wherein the reverse surface of the substrate is formed with terminals for connection to the through-holes and for bonding to an external mounting board; and

wherein the terminals are elongated to be substantially rectangular.

15

10. The infrared data communication module according to claim 9, wherein the terminals are so formed as to project from an obverse surface of the mounting board when the substrate is mounted on the mounting board with the reverse surface of the substrate oriented perpendicularly to the obverse surface of the substrate.

20

ABSTRACT

A method of making an infrared data communication module includes the steps of: forming predetermined wiring patterns on an obverse and a reverse surfaces (10a, 10b) of a substrate (1); mounting, on one of the surfaces of the substrate (1), a group of components including plural sets of light emitting elements (2) and light receiving elements (3); resin-packaging the group of components mounted on the substrate (1); and dividing the resin-packaged components to provide a plurality of infrared data communication modules (4) each of which includes a respective set of light emitting element and light receiving element. The resin-packaging step includes forming a plurality of mutually separated resin packages (4) each of which collectively seals at least two sets of light emitting elements (2) and light receiving elements (3).

Variable	Mean	SD	Min	Max
Age	34.5	10.2	21	55
Gender	0.5	0.5	0	1
Marital status	0.6	0.5	0	1
Education	12.5	1.5	9	16
Income	1500	500	500	3000
Health status	0.8	0.2	0	1
Smoking status	0.3	0.5	0	1
Alcohol consumption	0.2	0.4	0	1
Exercise frequency	0.5	0.5	0	1
Stress level	0.7	0.3	0	1
Sleep quality	0.6	0.4	0	1
Work satisfaction	0.5	0.5	0	1
Life satisfaction	0.6	0.4	0	1
Depression score	10.5	5.0	0	30
Anxiety score	12.0	6.0	0	30
Quality of life score	75.0	10.0	50	100



FIG. 2

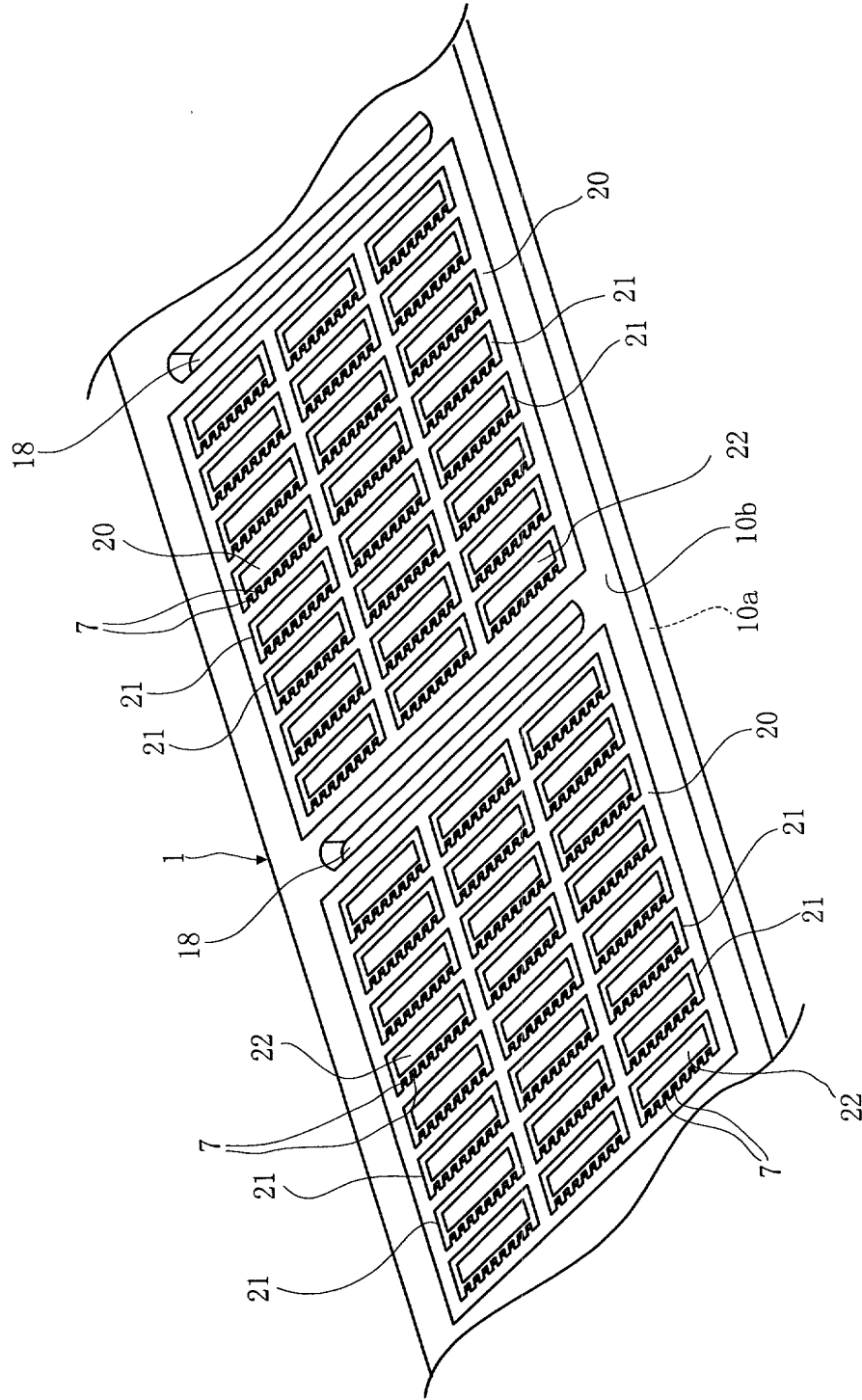


FIG.3

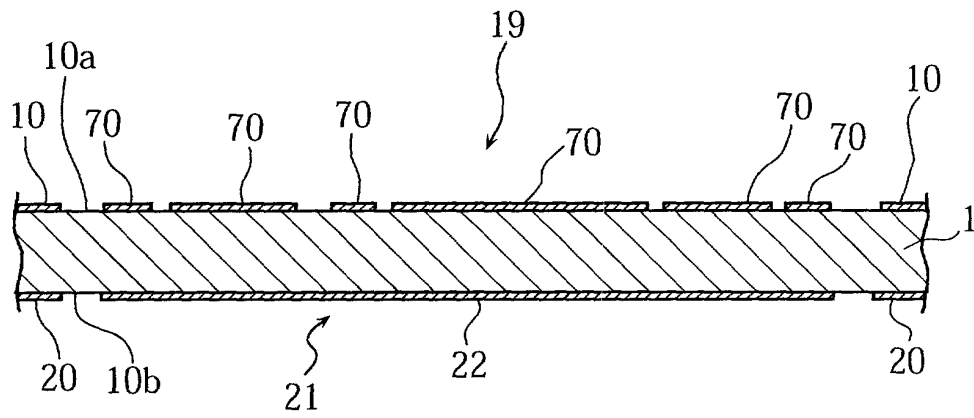


FIG.4

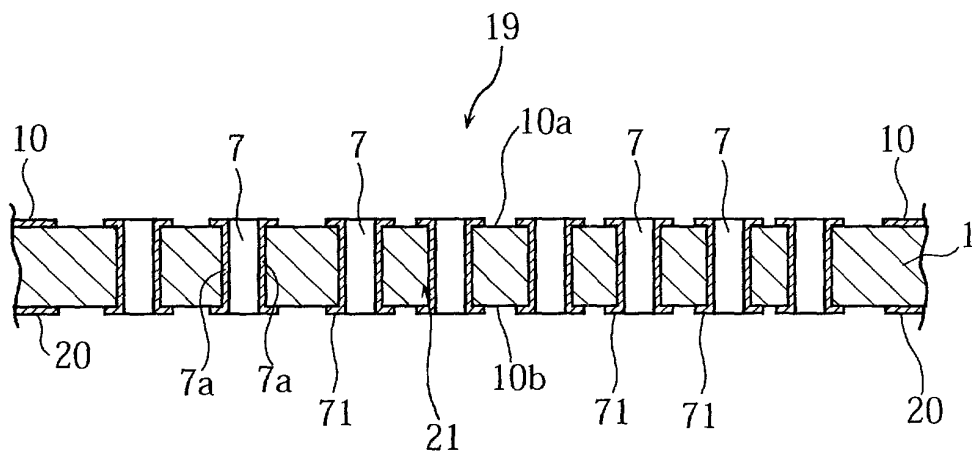


FIG. 5

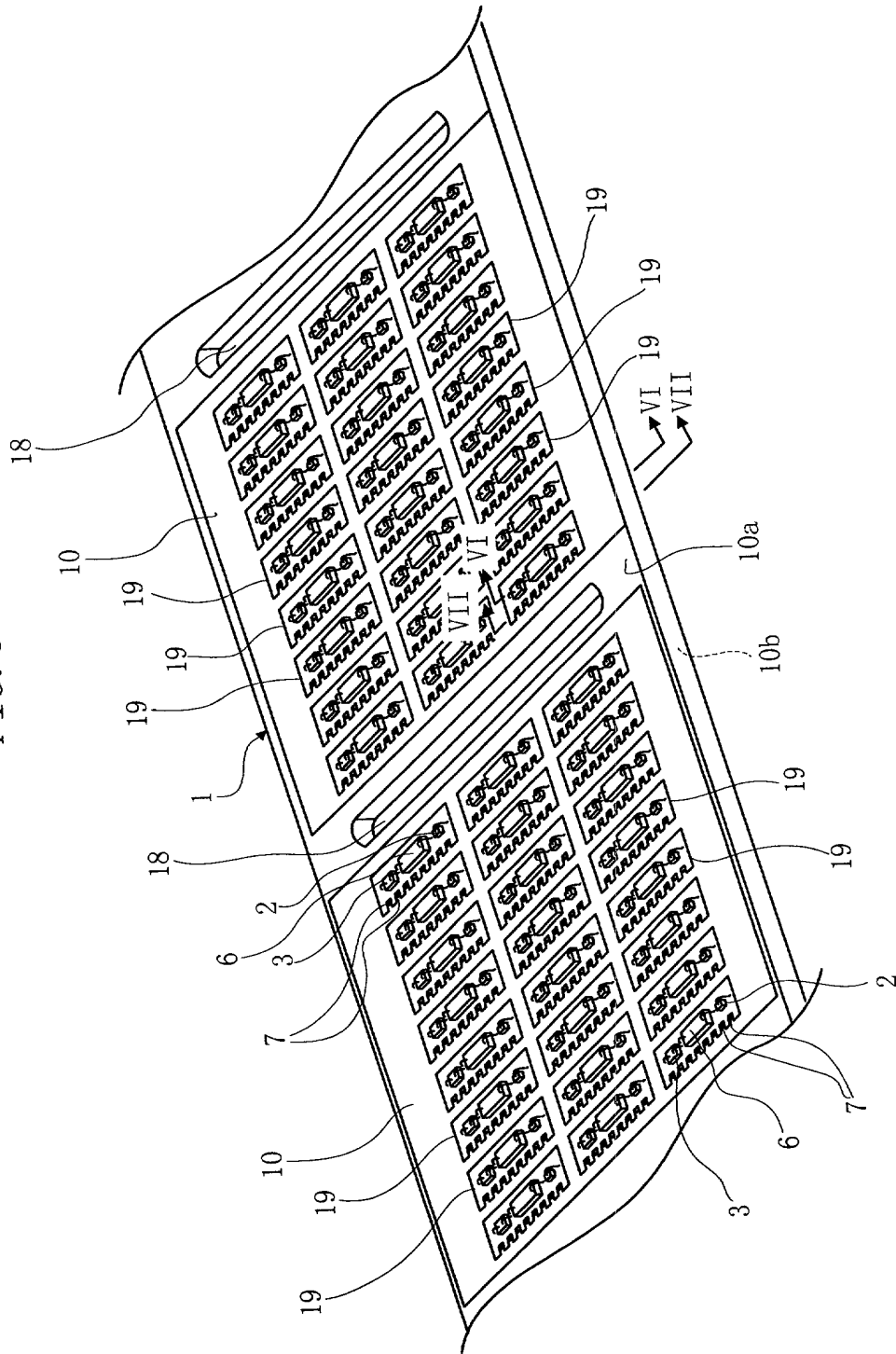


FIG.6

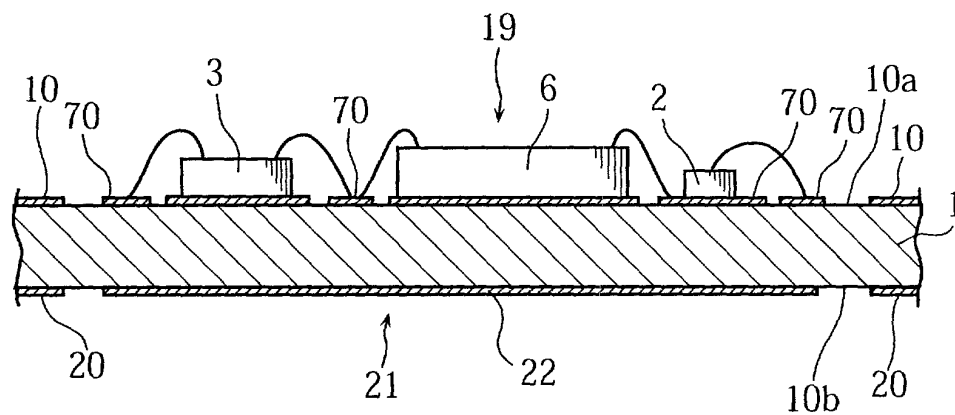


FIG.7

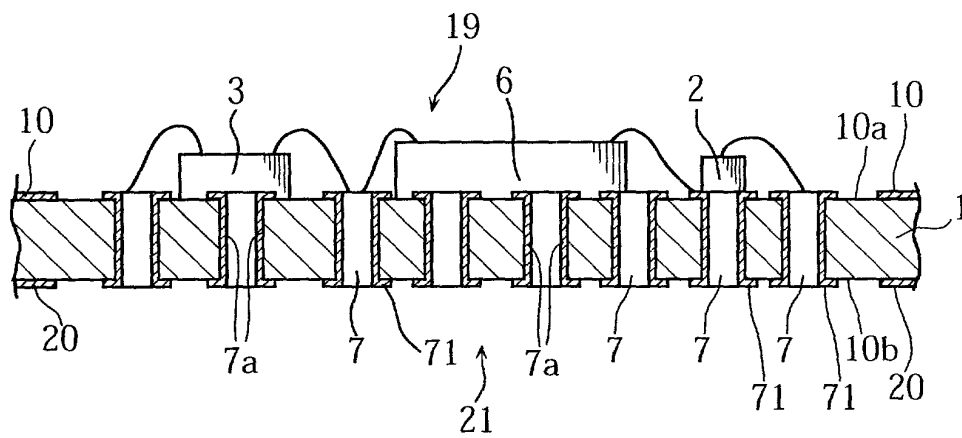


FIG. 8

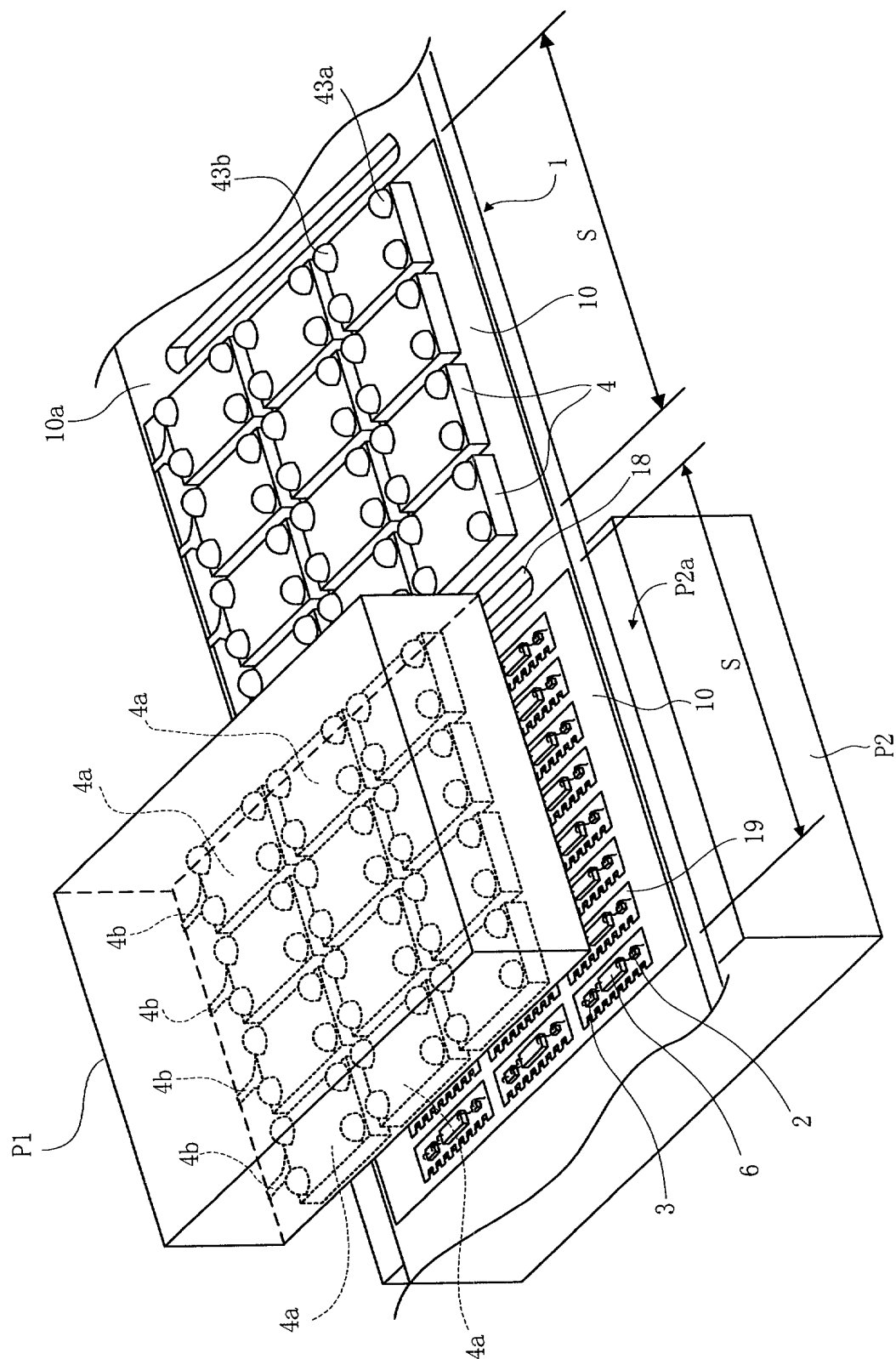


FIG. 9

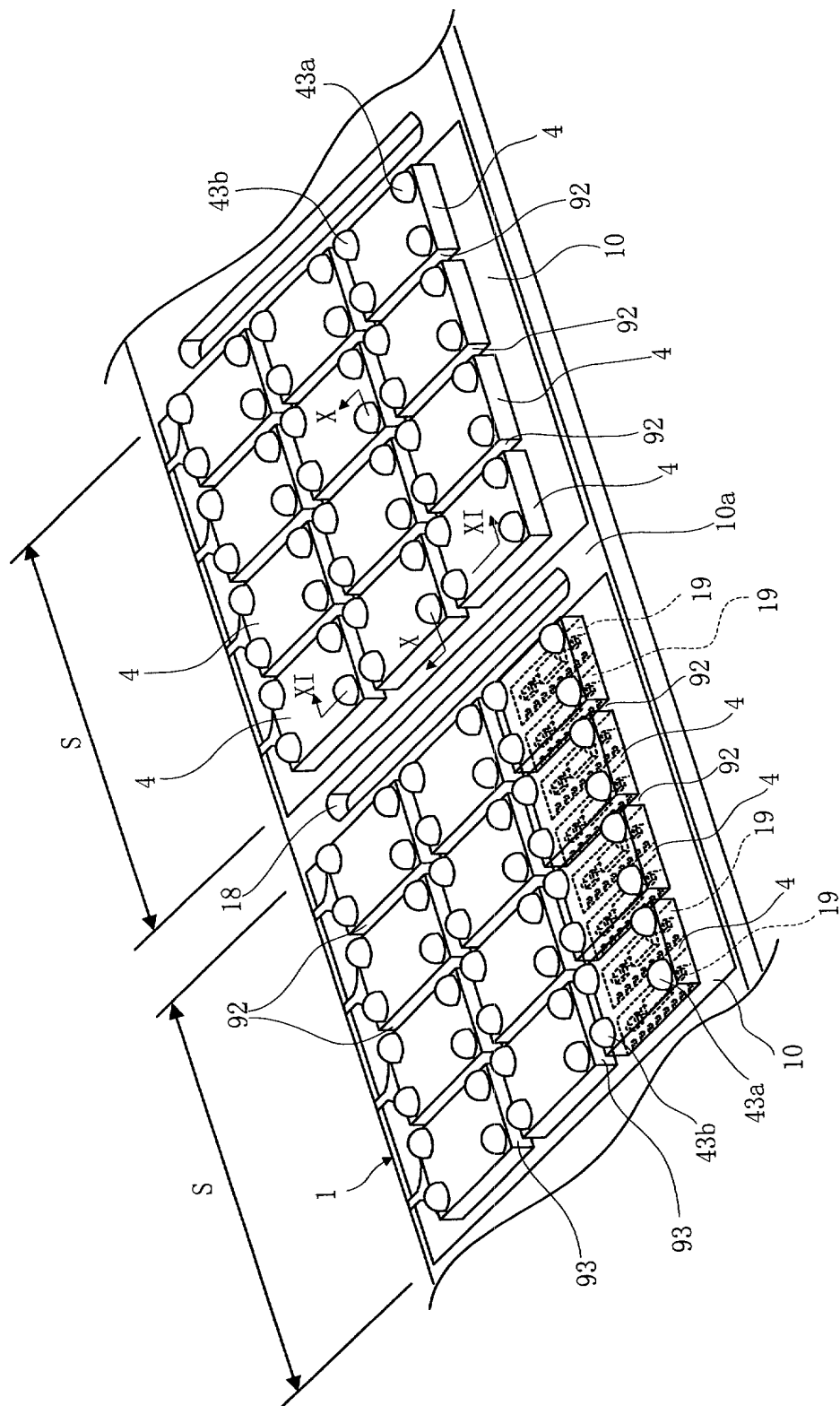


FIG. 10

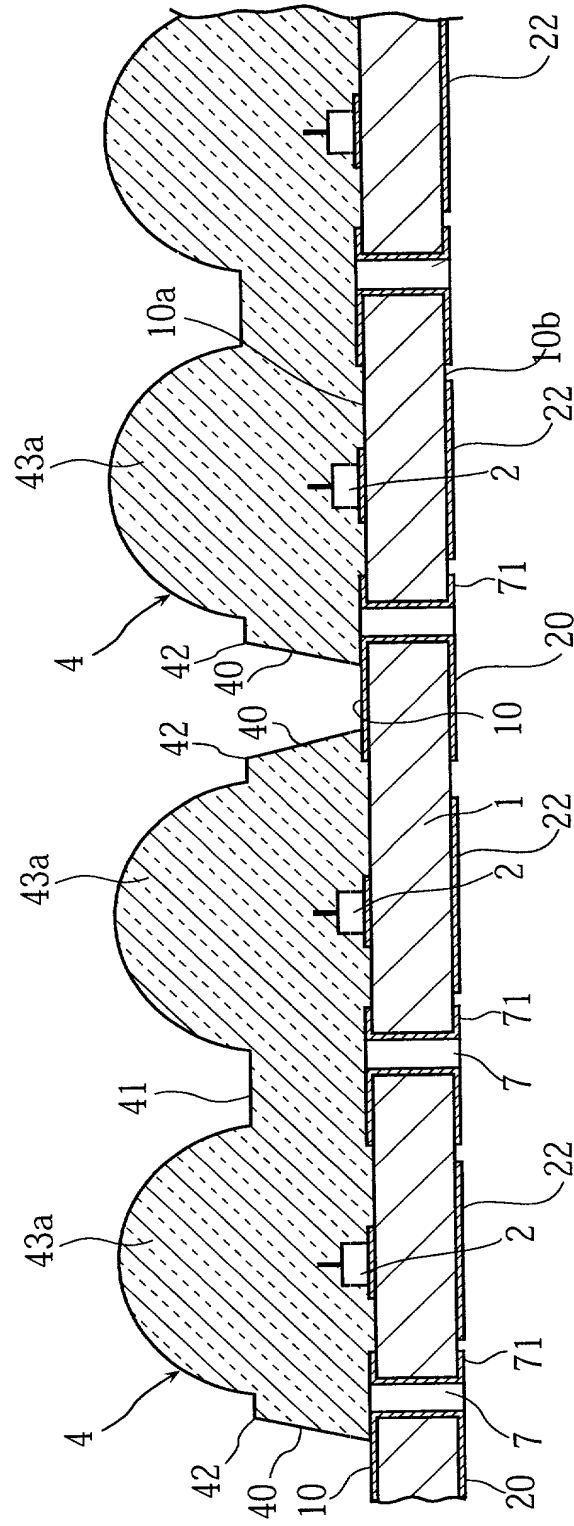


FIG.11

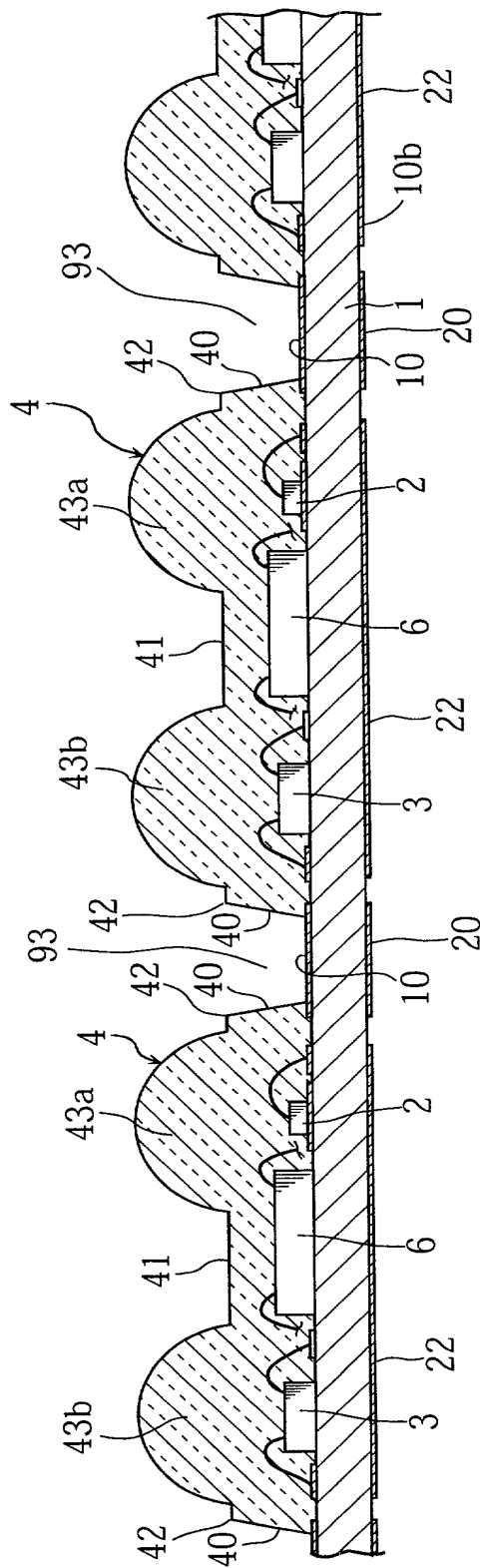
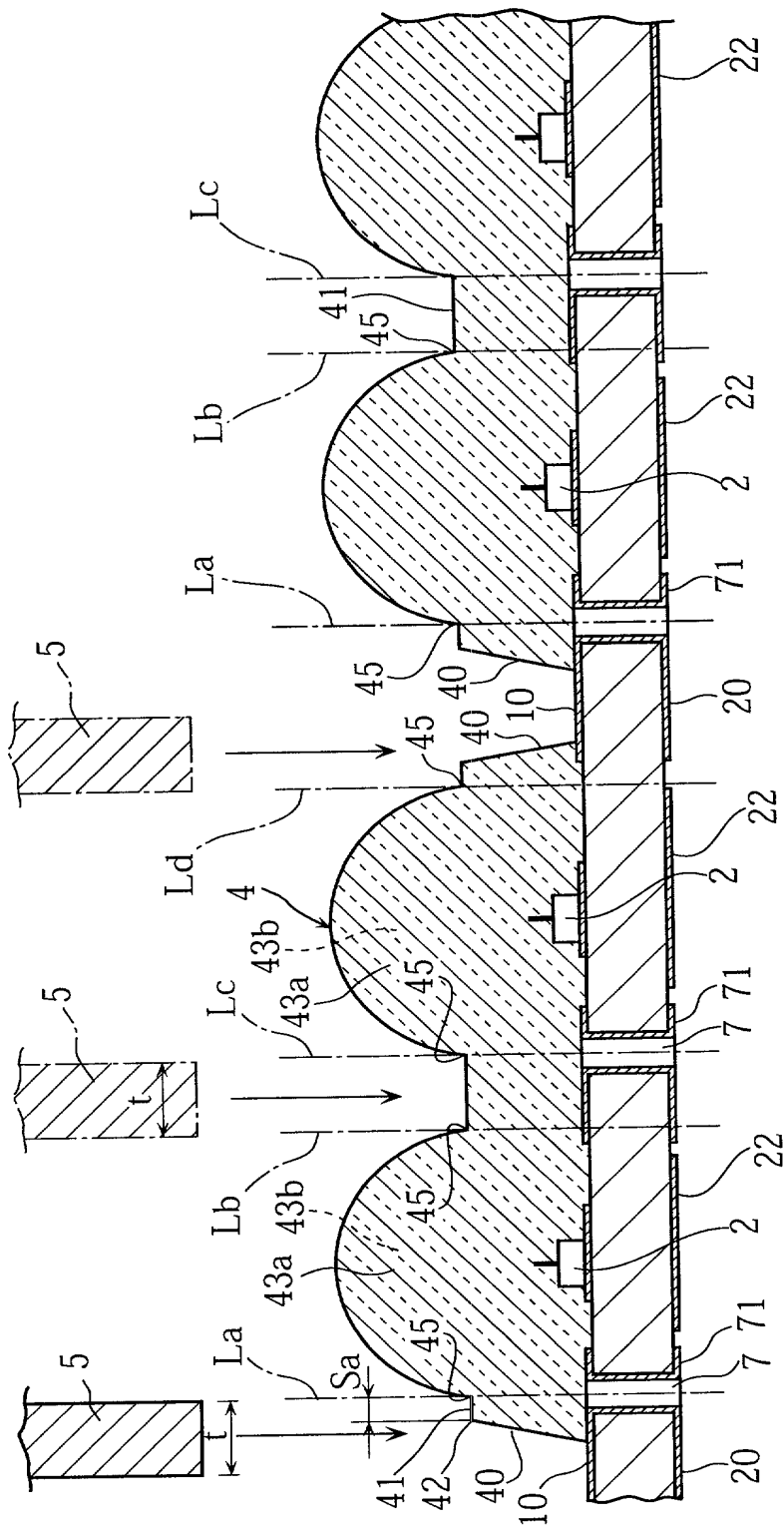


FIG.12



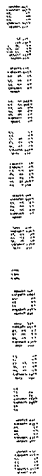
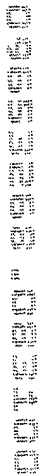
[illegible][illegible]

FIG.16

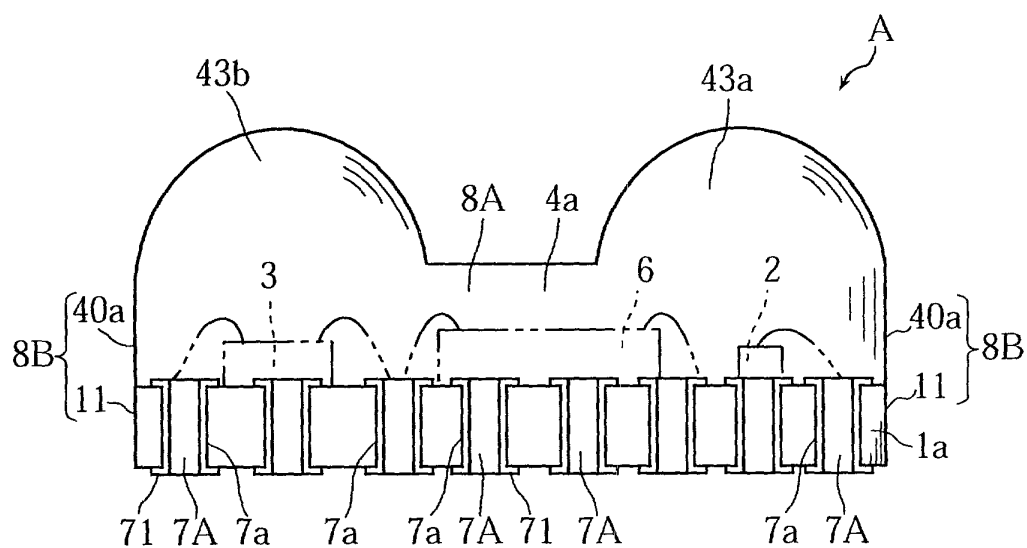
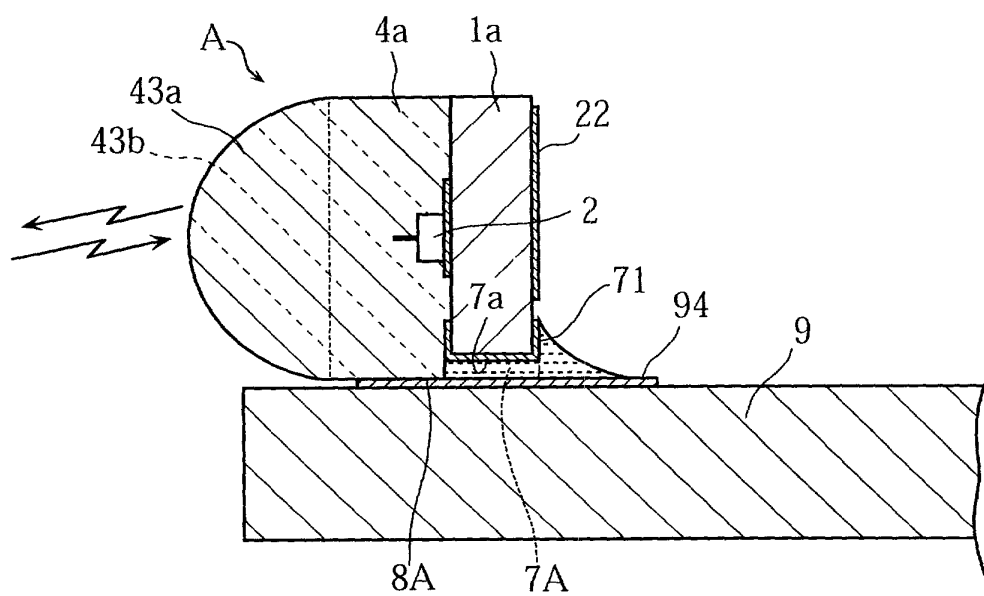
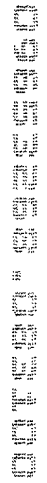


FIG.17



Overall mean		Overall SD	
Age	21.5	1.5	
Gender	50% M		
Height	1.70	0.05	
Weight	65.0	10.0	
Prevalence	10.0%		
Prevalence by age		Prevalence by gender	
Age	Prevalence	Gender	Prevalence
18-20	5.0%	M	10.0%
21-25	10.0%	F	5.0%
26-30	15.0%		
31-35	20.0%		
36-40	25.0%		
41-45	30.0%		
46-50	35.0%		
51-55	40.0%		
56-60	45.0%		
61-65	50.0%		
66-70	55.0%		
71-75	60.0%		
76-80	65.0%		
81-85	70.0%		
86-90	75.0%		
91-95	80.0%		
96-100	85.0%		
101-105	90.0%		
106-110	95.0%		
111-115	100.0%		
116-120	105.0%		
121-125	110.0%		
126-130	115.0%		
131-135	120.0%		
136-140	125.0%		
141-145	130.0%		
146-150	135.0%		
151-155	140.0%		
156-160	145.0%		
161-165	150.0%		
166-170	155.0%		
171-175	160.0%		
176-180	165.0%		
181-185	170.0%		
186-190	175.0%		
191-195	180.0%		
196-200	185.0%		
201-205	190.0%		
206-210	195.0%		
211-215	200.0%		
216-220	205.0%		
221-225	210.0%		
226-230	215.0%		
231-235	220.0%		
236-240	225.0%		
241-245	230.0%		
246-250	235.0%		
251-255	240.0%		
256-260	245.0%		
261-265	250.0%		
266-270	255.0%		
271-275	260.0%		
276-280	265.0%		
281-285	270.0%		
286-290	275.0%		
291-295	280.0%		
296-300	285.0%		
301-305	290.0%		
306-310	295.0%		
311-315	300.0%		
316-320	305.0%		
321-325	310.0%		
326-330	315.0%		
331-335	320.0%		
336-340	325.0%		
341-345	330.0%		
346-350	335.0%		
351-355	340.0%		
356-360	345.0%		
361-365	350.0%		
366-370	355.0%		
371-375	360.0%		
376-380	365.0%		
381-385	370.0%		
386-390	375.0%		
391-395	380.0%		
396-400	385.0%		
401-405	390.0%		
406-410	395.0%		
411-415	400.0%		
416-420	405.0%		
421-425	410.0%		
426-430	415.0%		
431-435	420.0%		
436-440	425.0%		
441-445	430.0%		
446-450	435.0%		
451-455	440.0%		
456-460	445.0%		
461-465	450.0%		
466-470	455.0%		
471-475	460.0%		
476-480	465.0%		
481-485	47		



Overall mean		Overall SD	
Age	21.5	1.5	
Gender	50% M		
Height	1.70	0.05	
Weight	65.0	10.0	
Prevalence	10.0%		
Prevalence by age		Prevalence by gender	
Age	Prevalence	Gender	Prevalence
18-20	5.0%	M	10.0%
21-25	10.0%	F	5.0%
26-30	15.0%		
31-35	20.0%		
36-40	25.0%		
41-45	30.0%		
46-50	35.0%		
51-55	40.0%		
56-60	45.0%		
61-65	50.0%		
66-70	55.0%		
71-75	60.0%		
76-80	65.0%		
81-85	70.0%		
86-90	75.0%		
91-95	80.0%		
96-100	85.0%		
101-105	90.0%		
106-110	95.0%		
111-115	100.0%		
116-120	105.0%		
121-125	110.0%		
126-130	115.0%		
131-135	120.0%		
136-140	125.0%		
141-145	130.0%		
146-150	135.0%		
151-155	140.0%		
156-160	145.0%		
161-165	150.0%		
166-170	155.0%		
171-175	160.0%		
176-180	165.0%		
181-185	170.0%		
186-190	175.0%		
191-195	180.0%		
196-200	185.0%		
201-205	190.0%		
206-210	195.0%		
211-215	200.0%		
216-220	205.0%		
221-225	210.0%		
226-230	215.0%		
231-235	220.0%		
236-240	225.0%		
241-245	230.0%		
246-250	235.0%		
251-255	240.0%		
256-260	245.0%		
261-265	250.0%		
266-270	255.0%		
271-275	260.0%		
276-280	265.0%		
281-285	270.0%		
286-290	275.0%		
291-295	280.0%		
296-300	285.0%		
301-305	290.0%		
306-310	295.0%		
311-315	300.0%		
316-320	305.0%		
321-325	310.0%		
326-330	315.0%		
331-335	320.0%		
336-340	325.0%		
341-345	330.0%		
346-350	335.0%		
351-355	340.0%		
356-360	345.0%		
361-365	350.0%		
366-370	355.0%		
371-375	360.0%		
376-380	365.0%		
381-385	370.0%		
386-390	375.0%		
391-395	380.0%		
396-400	385.0%		
401-405	390.0%		
406-410	395.0%		
411-415	400.0%		
416-420	405.0%		
421-425	410.0%		
426-430	415.0%		
431-435	420.0%		
436-440	425.0%		
441-445	430.0%		
446-450	435.0%		
451-455	440.0%		
456-460	445.0%		
461-465	450.0%		
466-470	455.0%		
471-475	460.0%		
476-480	465.0%		
481-485	47		

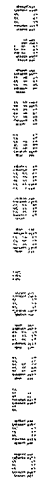


FIG.20
PRIOR ART

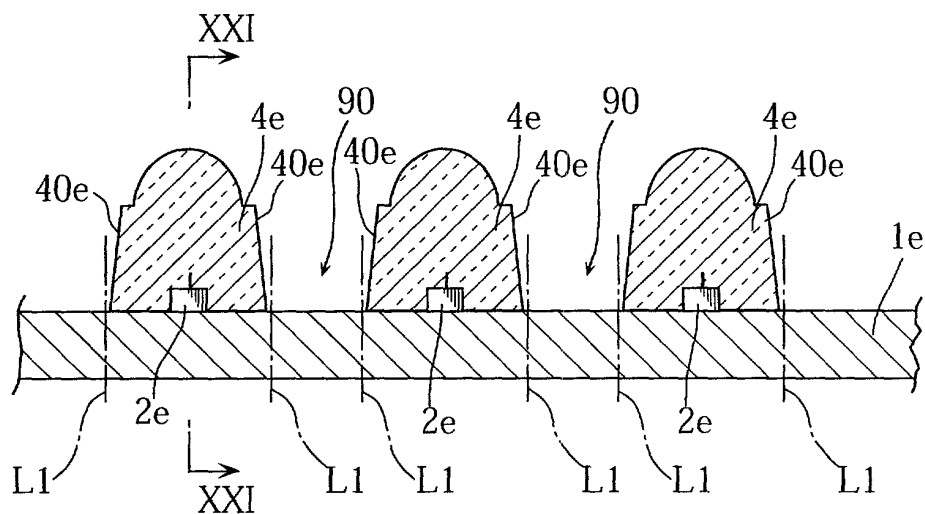


FIG.21
PRIOR ART

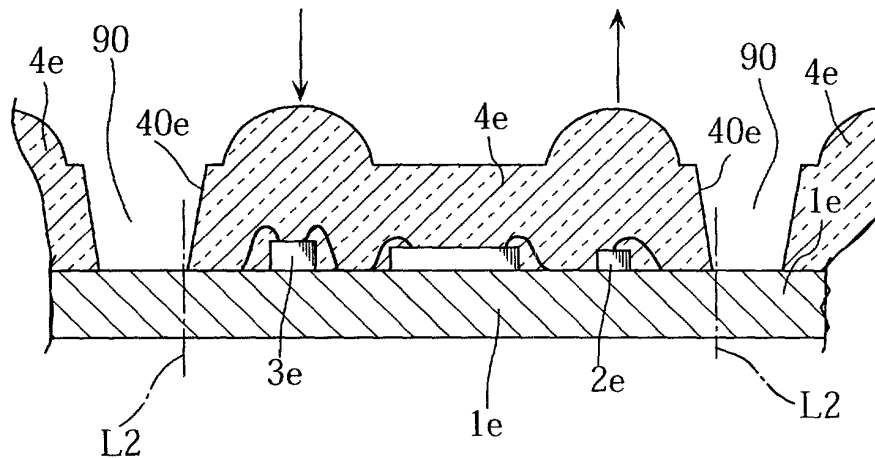


FIG.22
PRIOR ART

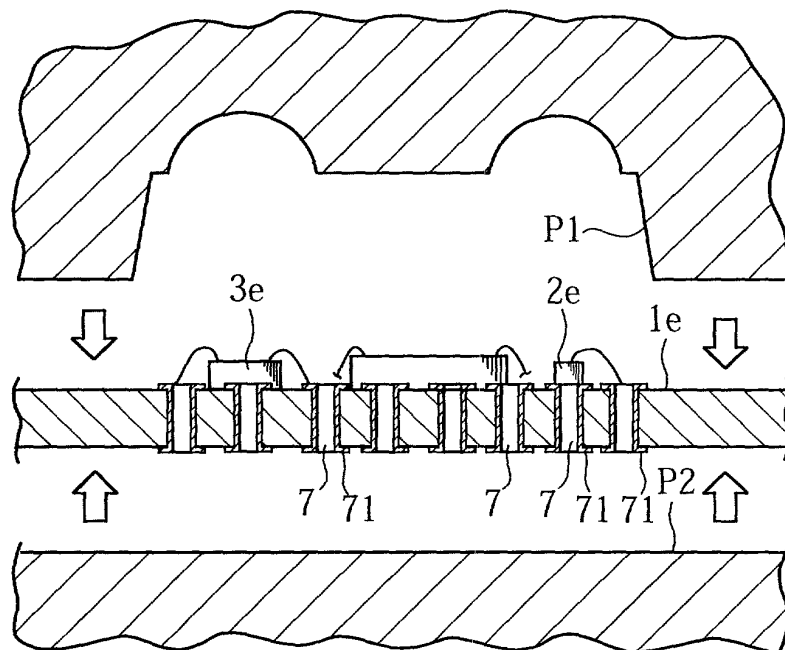
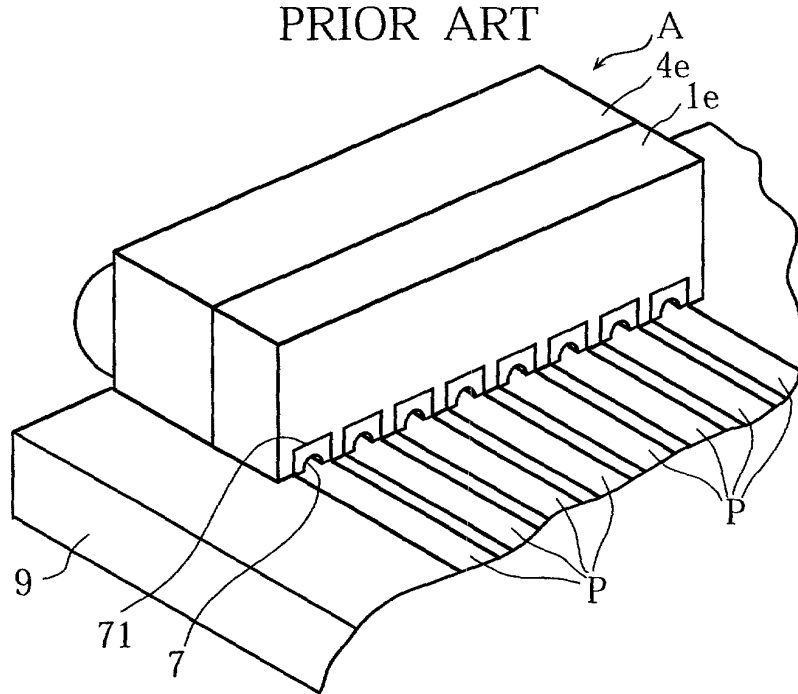


FIG.23
PRIOR ART



Declaration and Power of Attorney For Patent Application

特許出願宣言書

Japanese Language Declaration

私は、下欄に氏名を記載した発明者として、以下のとおり宣言する：

As a below named inventor, I hereby declare that:

私の住所、郵便の宛先および国籍は、下欄に氏名に続いて記載したとおりであり、

My residence, post office address and citizenship are as stated below next to my name,

名称の発明に関し、請求の範囲に記載した特許を求める主題の本来の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本来の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

INFRARED DATA COMMUNICATION MODULE
AND METHOD OF MAKING THE SAME

その明細書を
（該当する方に印を付す）

the specification of which
(check one)

☐ここに添付する。

☒is attached hereto.

☐ _____ 日に出願番号
第 _____ 号として提出し、
_____ 日に補正した。
(該当する場合)

☐was filed on _____ as
Application Serial No. _____
and was amended on _____
(if applicable)

☐ _____ 日にPCT国際出願番号
第 _____ 号として提出し、
PCT第19条に基づき _____ 日に補正した。
(該当する場合)

☐was described and claimed in PCT international application
No. _____ filed on _____
and as amended under PCT Article 19 on _____
(if applicable)

私は、前記のとおり補正した請求の範囲を含む前記明細書の内容を検討し、理解したことを陳述する。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37部第1章第56条(a)項に従い、本願の審査に所要の情報を開示すべき義務を有することを認める。

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

Japanese Language Declaration

私は、合衆国法典第35部第119条 (a) - (d) 項または第365条 (a) - (b) 項にもとづく下記の外国特許出願または発明者証出願または少なくとも1つの合衆国以外の国を指定したPCT国際出願の外国優先権利益を主張し、さらに優先権の主張に係わる基礎出願の出願日前の出願日を有する外国特許出願または発明者証出願またはPCT国際出願を以下に明記する：

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(a)-(b) of any foreign application(s) for patent or inventor's certificate, or of any PCT international application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate or PCT international application having a filing date before that of the application on which priority is claimed:

Prior foreign applications

先の外国出願

(Number) (番号)	(Country) (国名)	(Day/Month/Year Filed) (出願の年月日)
Patent Application No. 11-245481	Japan	31/8/1999
(Number) (番号)	(Country) (国名)	(Day/Month/Year Filed) (出願の年月日)
Patent Application No. 11-254268	Japan	8/9/1999
(Number) (番号)	(Country) (国名)	(Day/Month/Year Filed) (出願の年月日)
Patent Application No. 11-256663	Japan	10/9/1999
(Number) (番号)	(Country) (国名)	(Day/Month/Year Filed) (出願の年月日)
(Number) (番号)	(Country) (国名)	(Day/Month/Year Filed) (出願の年月日)

Priority claimed

優先権の主張

<input checked="" type="checkbox"/>	<input type="checkbox"/>
Yes あり	No なし
<input checked="" type="checkbox"/>	<input type="checkbox"/>
Yes あり	No なし
<input checked="" type="checkbox"/>	<input type="checkbox"/>
Yes あり	No なし
<input type="checkbox"/>	<input type="checkbox"/>
Yes あり	No なし
<input type="checkbox"/>	<input type="checkbox"/>
Yes あり	No なし

私は、合衆国法典第35部第120条にもとづく下記の合衆国特許出願の利益または第365条 (c) 項にもとづく合衆国を指定するPCT国際出願の利益を主張し、本願の請求の範囲各項に記載の主題が合衆国法典第35部第112条第1項に規定の様で先の合衆国出願に開示されていない限度において、先の出願の出願日と本願の国内出願日またはPCT国際出願日の間に公表された連邦規則法典第37部第1章第56条 (a) 項に記載の所要の情報を開示すべき義務を有することを認める：

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s), or § 365(c) of any PCT international application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)
(出願番号)

(Filing Date)
(出願日)

(現況) (Status)
(特許済み、係属中、放棄済み) (patented, pending, abandoned)

(Application Serial No.)
(出願番号)

(Filing Date)
(出願日)

(現況) (Status)
(特許済み、係属中、放棄済み) (patented, pending, abandoned)

Japanese Language Declaration

私は、ここに自己の知識にもとづいて行った陳述がすべて真実であり、自己の有する情報および信ずるところに従って行った陳述が真実であると信じ、さらに故意に虚偽の陳述等を行った場合、合衆国法典第18部第1001条により、罰金もしくは禁固に処せられるか、またはこれらの刑が併科され、またかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損なうことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

委任状：私は、下記発明者として、以下の代理人をここに選任し、本願の手続を遂行すること並びにこれに関する一切の行為を特許商標庁に対して行うことを委任する。
(代理人氏名および登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and /or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. *(list name and registration number)*

Michael D. Bednarek, Regn. No. 32, 329

Lawrence J. Gotts, Regn. No. 31, 163

Aslan Baghdadi, Regn. No. 34, 542

Elizabeth M. Roesel, Regn. No. 34, 878

David C. Isaacson, Regn. No. 38, 500

書類の送付先：

Michael D. Bednarek
SHAW PITTMAN
2300 N Street, N.W.,
Washington, D.C. 20037-1138

Send Correspondence to:

Michael D. Bednarek
SHAW PITTMAN
2300 N Street, N.W.,
Washington, D.C. 20037-1138

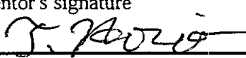
直通電話連絡先：(名称及び電話番号)

Michael D. Bednarek at 202/663-8904

Direct Telephone Calls to: *(name and telephone number)*

Michael D. Bednarek at 202/663-8904

Japanese Language Declaration

唯一のまたは第一の発明者の氏名	Full name of sole or first inventor Tomoharu Horio	
同発明者の署名	Inventor's signature 	Date August 25, 2000
住所	Residence c/o ROHM CO., LTD. Kyoto, Japan	
国籍	Citizenship Japan	
郵便の宛先	Post Office Address c/o ROHM CO., LTD. 21, Saiin Mizosaki-cho, Ukyo-ku, Kyoto-shi, Kyoto 615-8585 Japan	

第2の共同発明者の氏名 (該当する場合)	Full name of second joint inventor, if any	
同第2発明者の署名	Second Inventor's signature	Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	

第3の共同発明者の氏名 (該当する場合)	Full name of third joint inventor, if any	
同第3発明者の署名	Third Inventor's signature	Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	

第4の共同発明者の氏名 (該当する場合)	Full name of fourth joint inventor, if any	
同第4発明者の署名	Fourth Inventor's signature	Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	

第5の共同発明者の氏名 (該当する場合)	Full name of fifth joint inventor, if any	
同第5発明者の署名	Fifth Inventor's signature	Date
住所	Residence	
国籍	Citizenship	
郵便の宛先	Post Office Address	